

Fig.1

FIG. 1 is a perspective view of a multi-layered rectangular structure.

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Fig.2A

Fig.2B

Fig.2C

FIG. 2A, 2B, and 2C are perspective views of a multi-layered electronic device assembly 10a, showing the top, middle, and bottom layers respectively. The assembly is shown in a perspective view, with dashed lines indicating internal features and components.

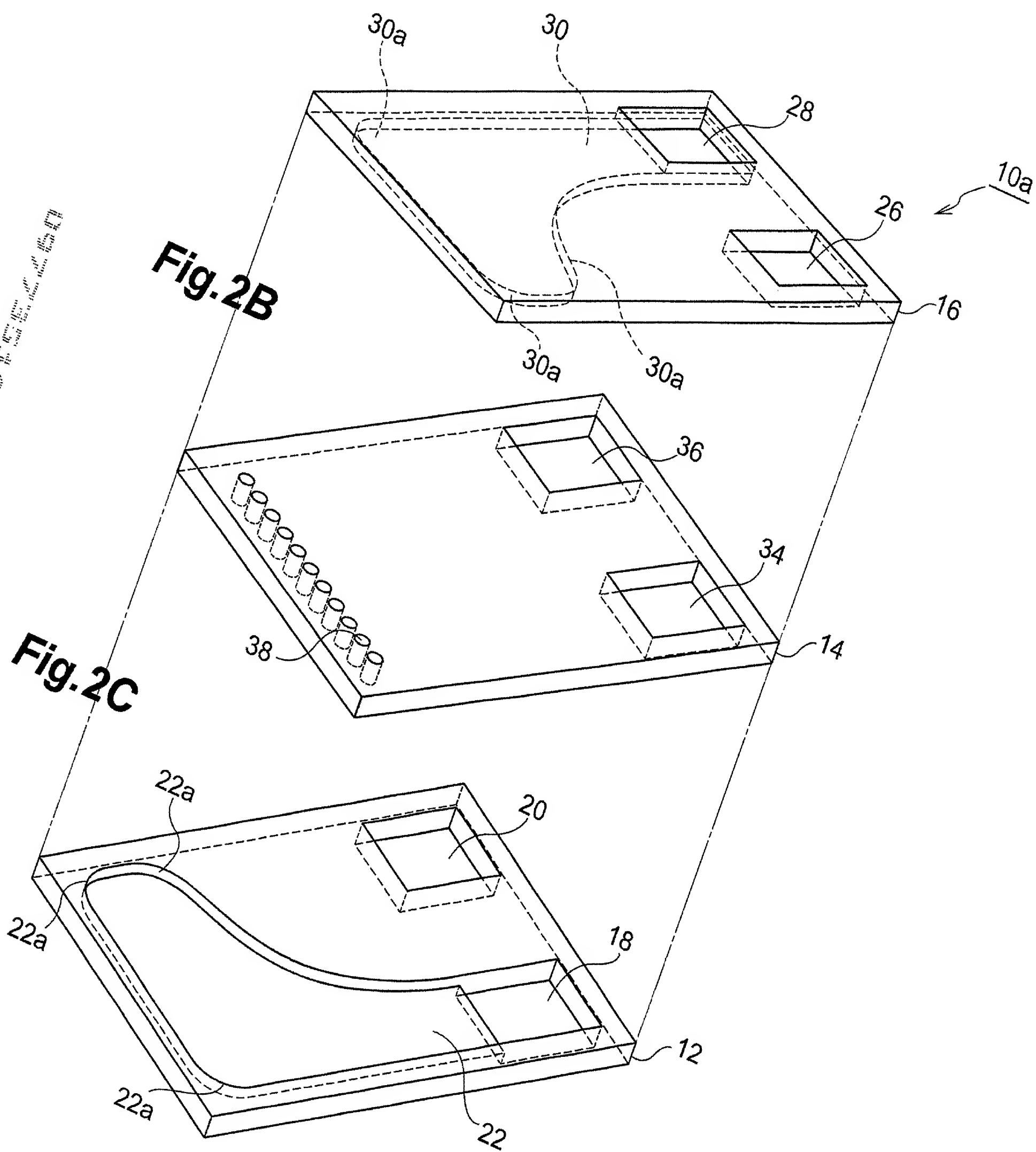


Fig.3

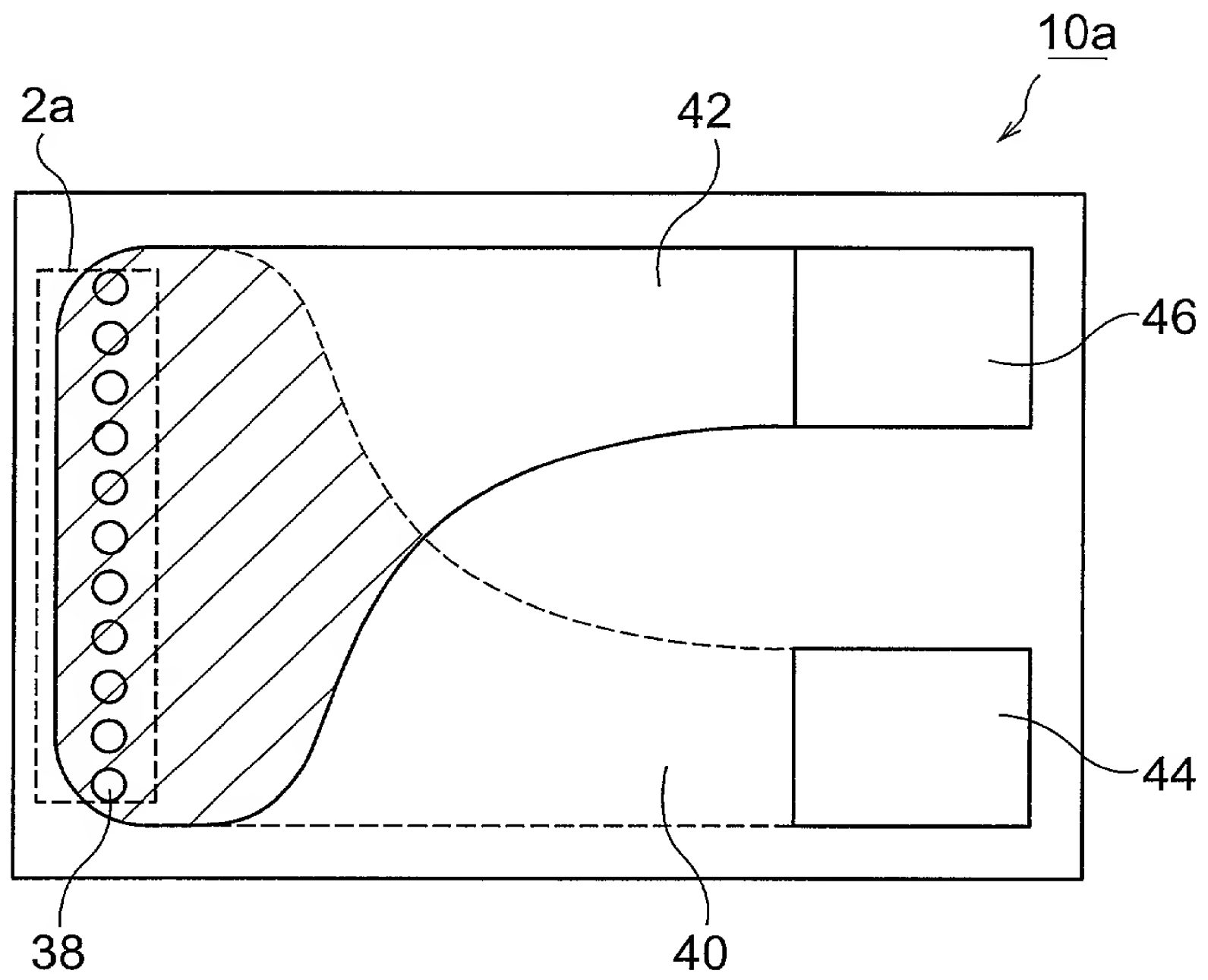


Fig.4

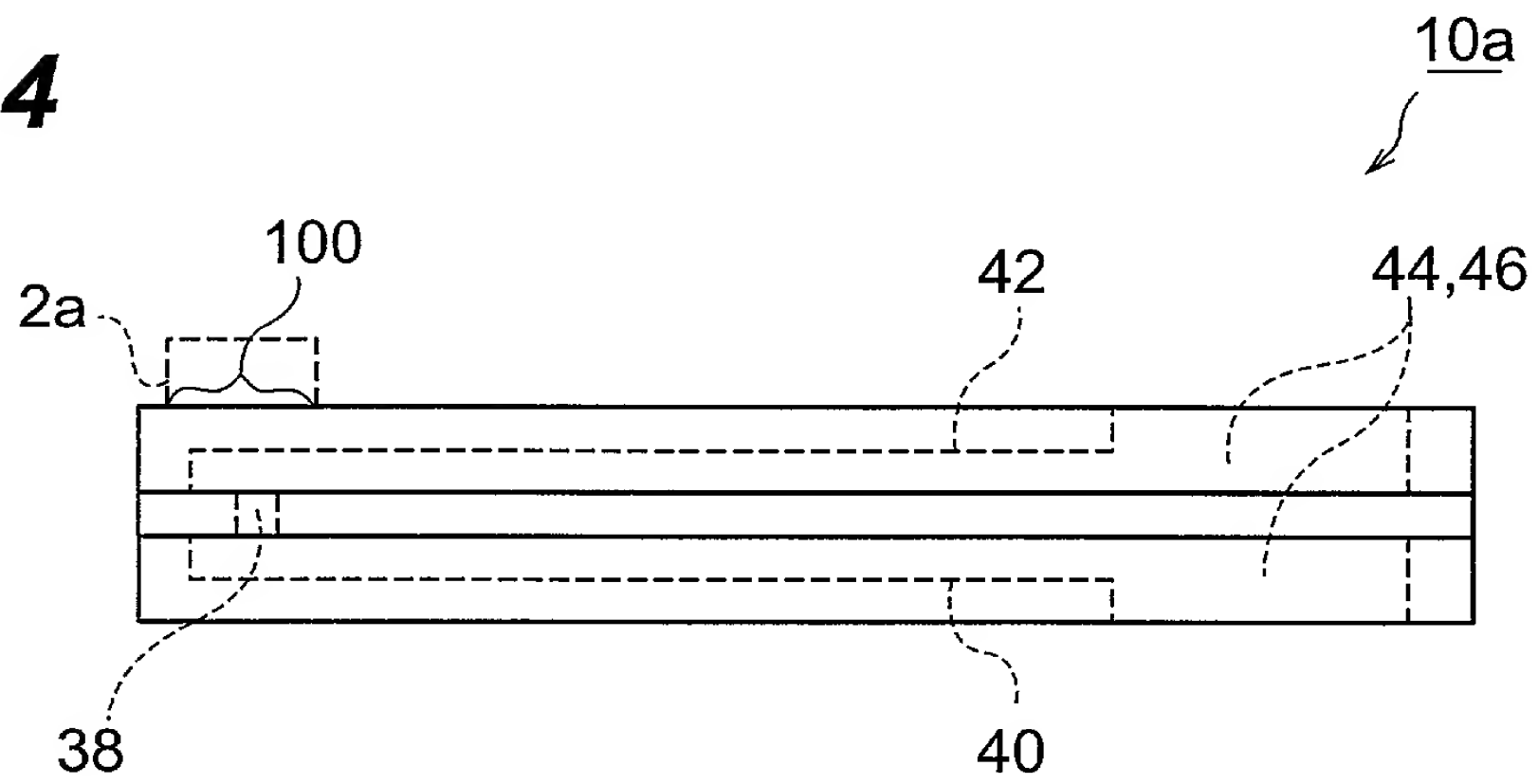


Fig.5

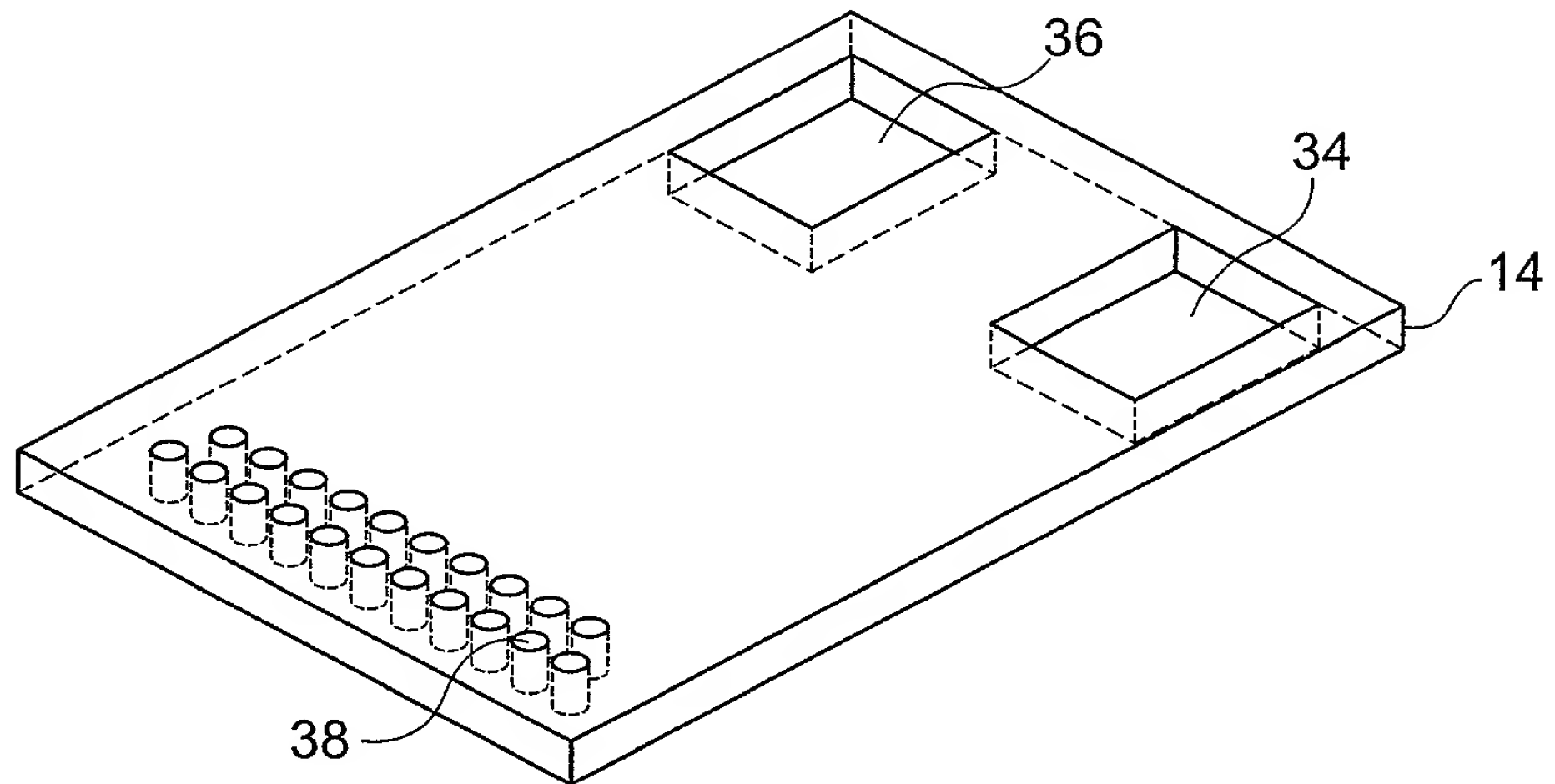


Fig.6

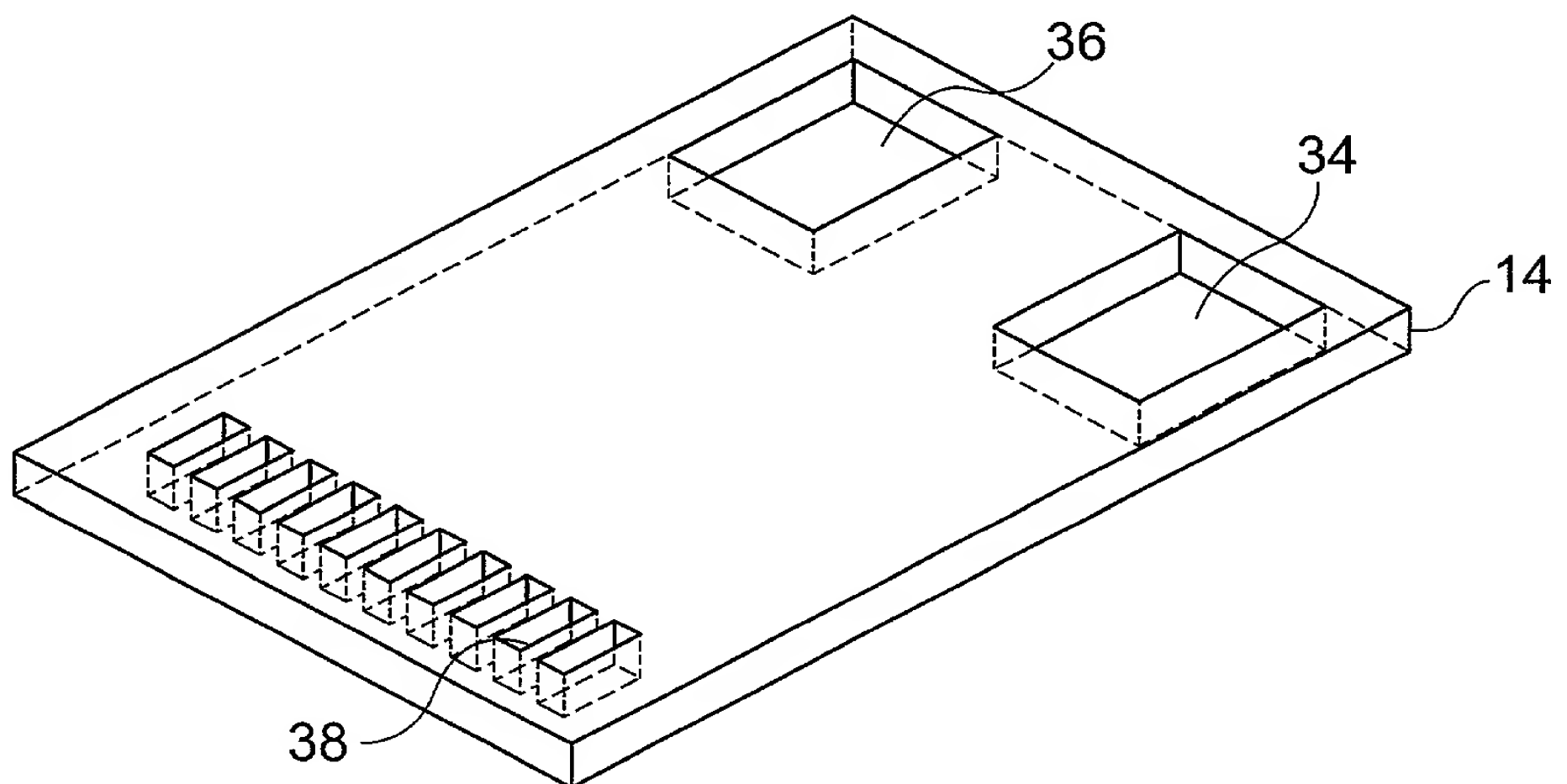


Fig. 7

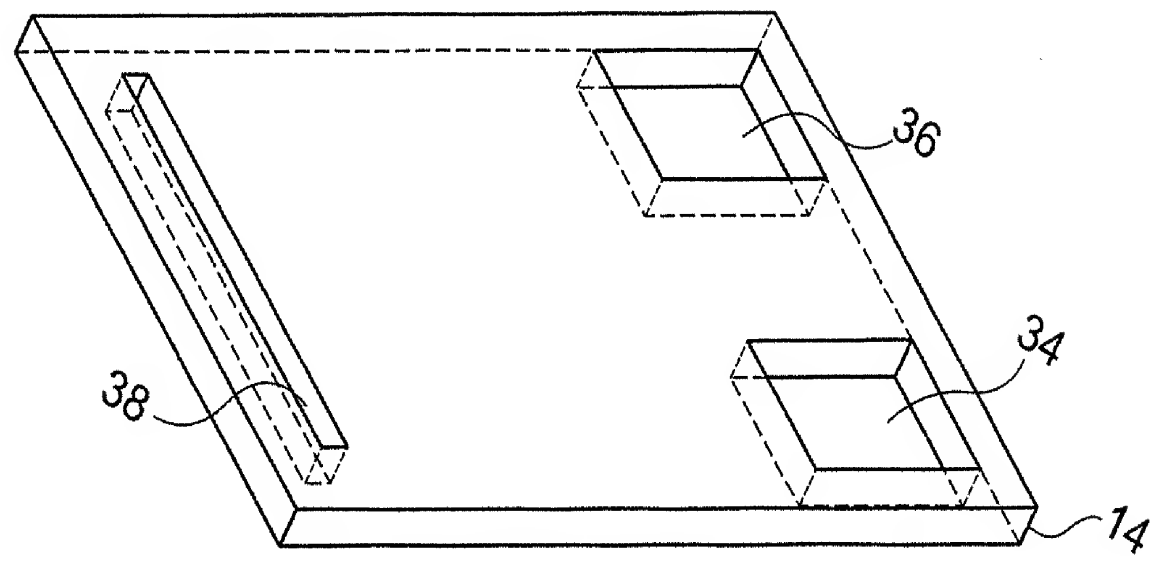


Fig. 8

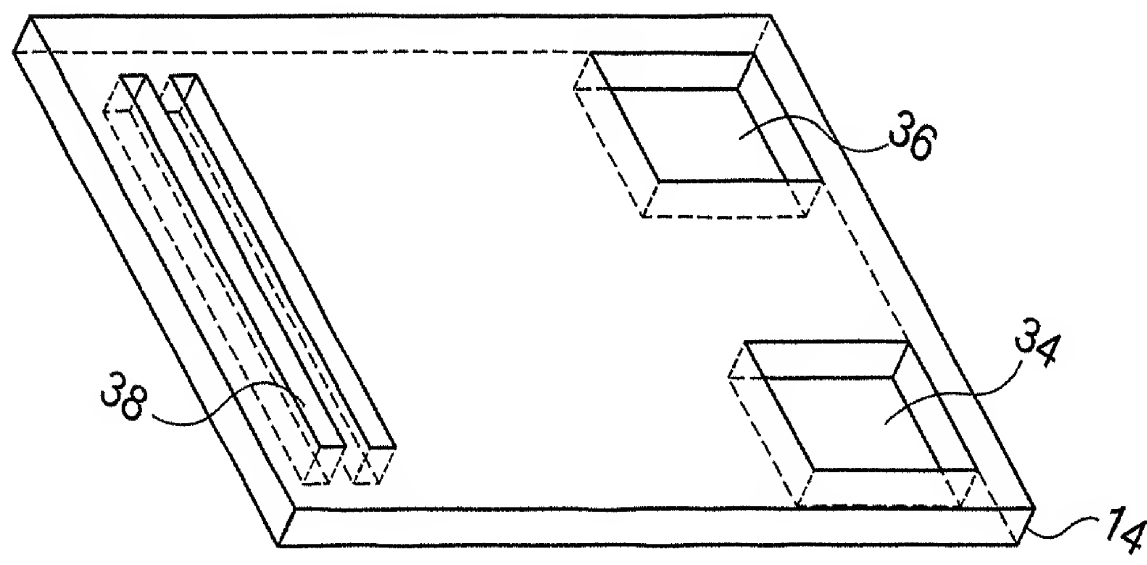


Fig.9A

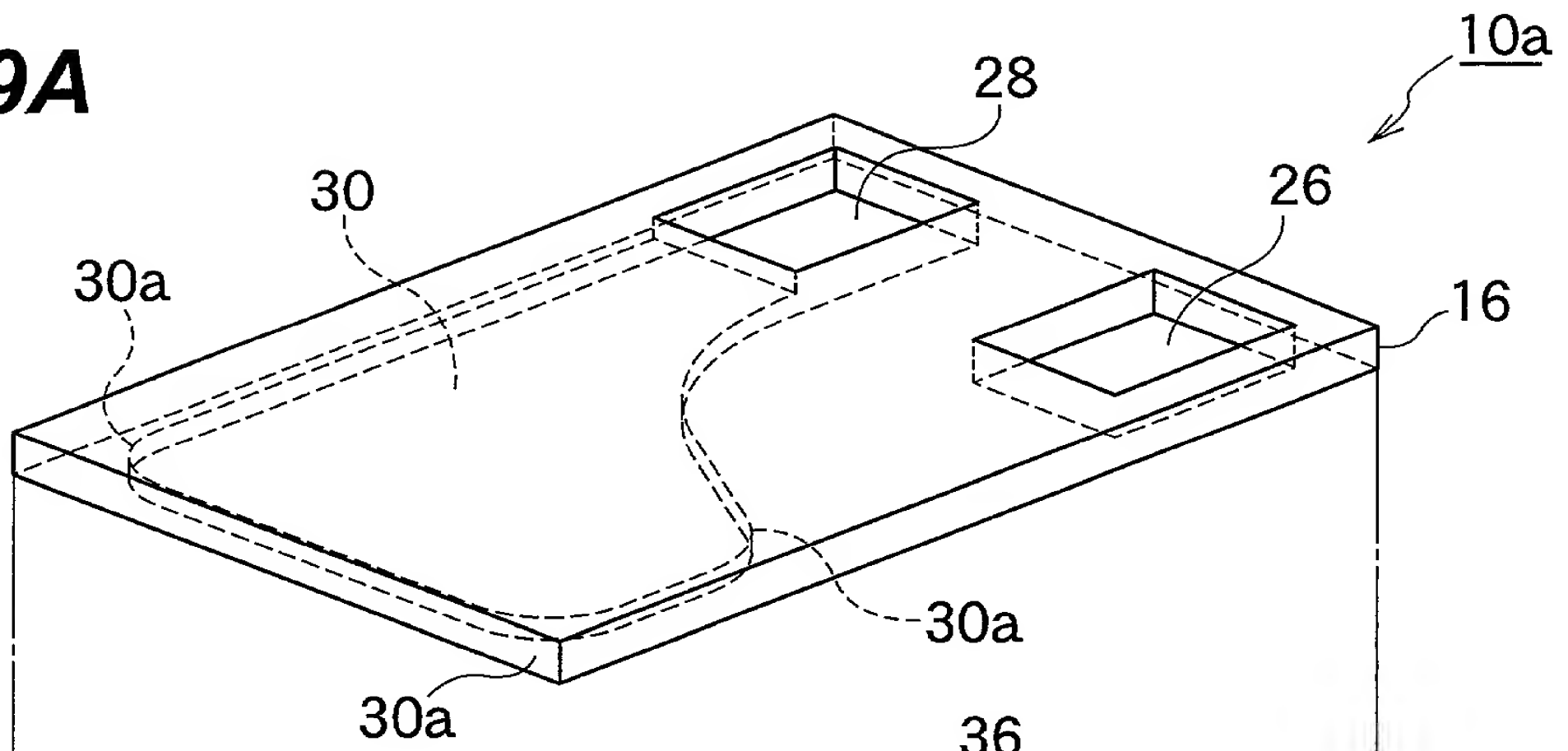


Fig.9B

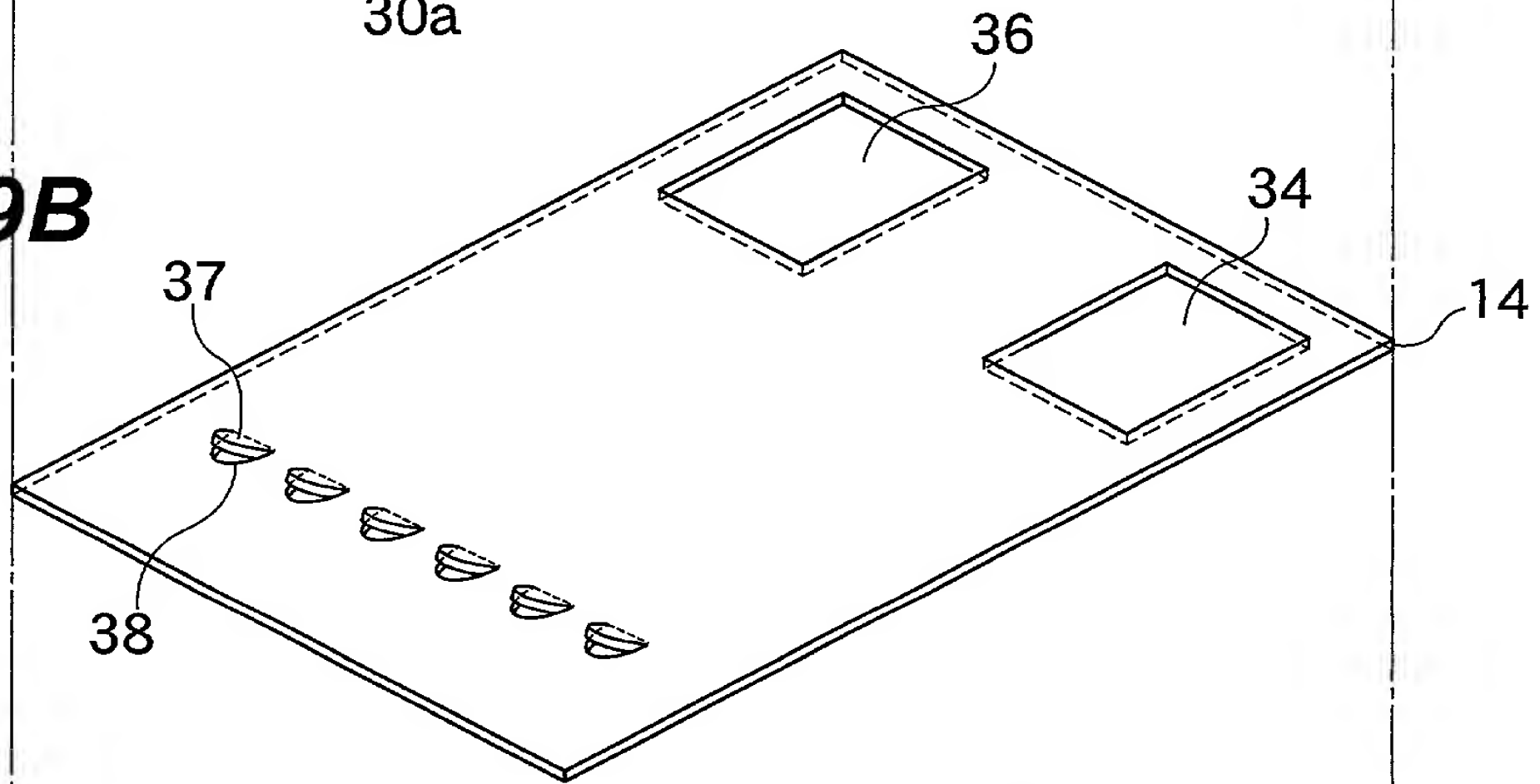


Fig.9C

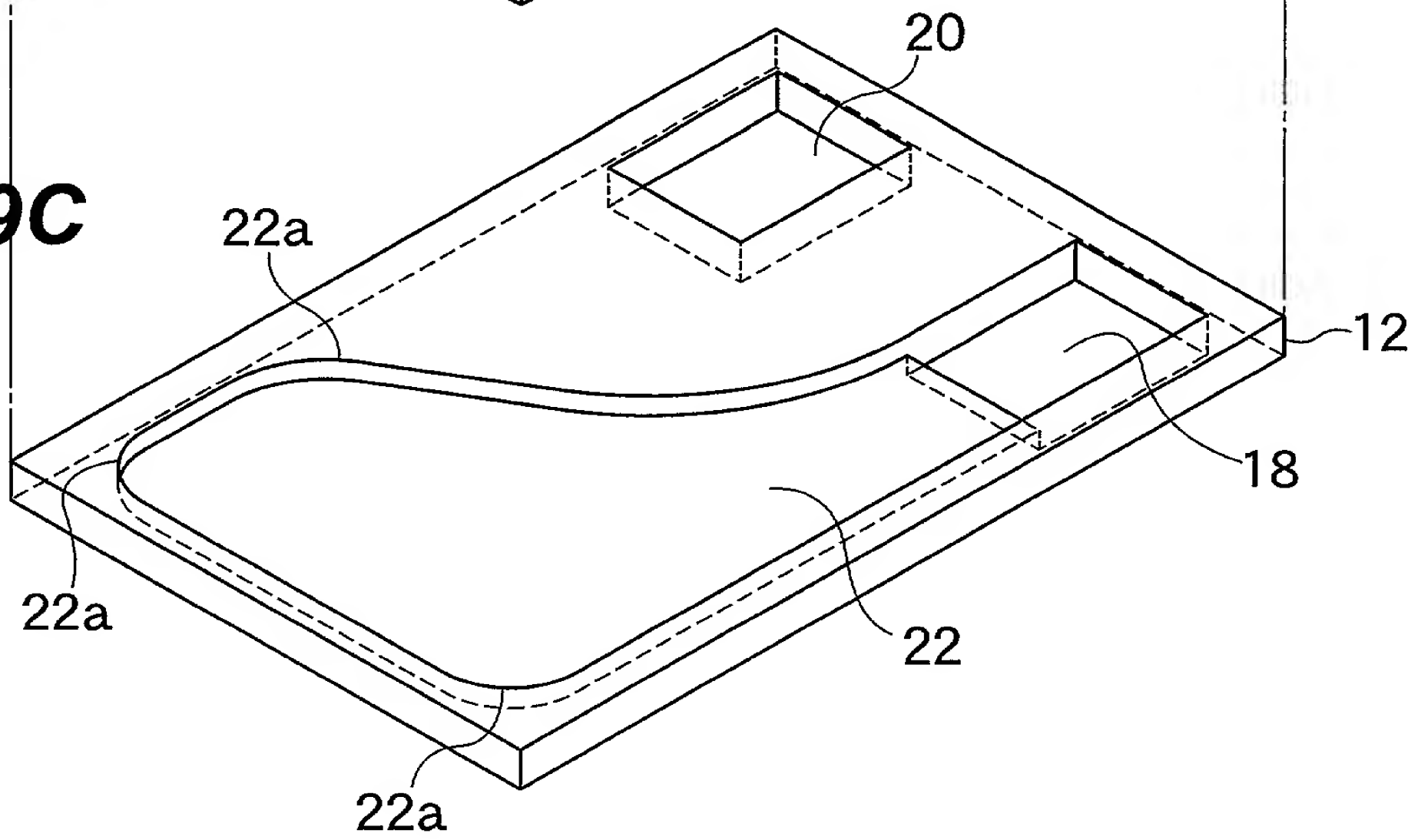


Fig.10

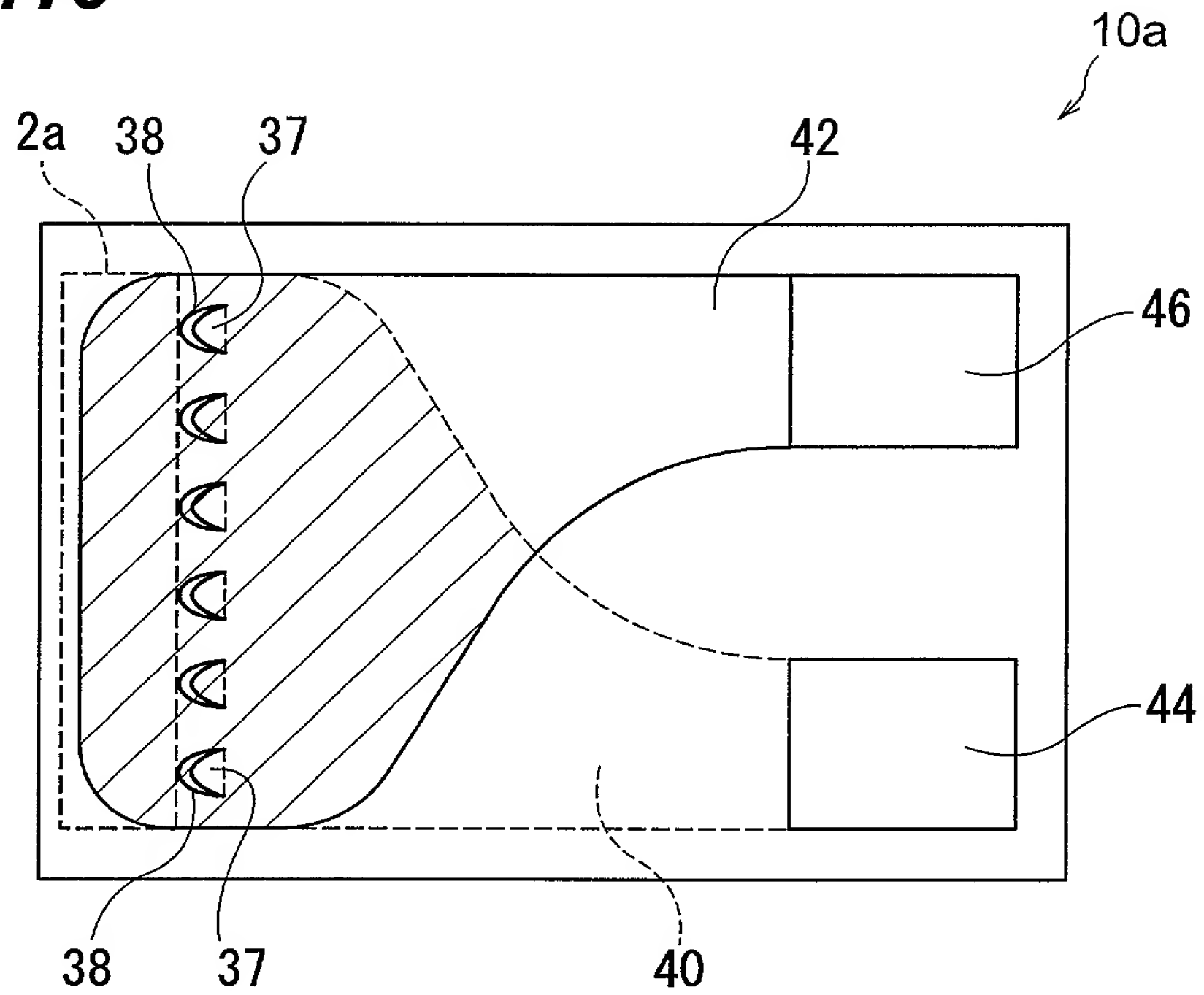


Fig.11

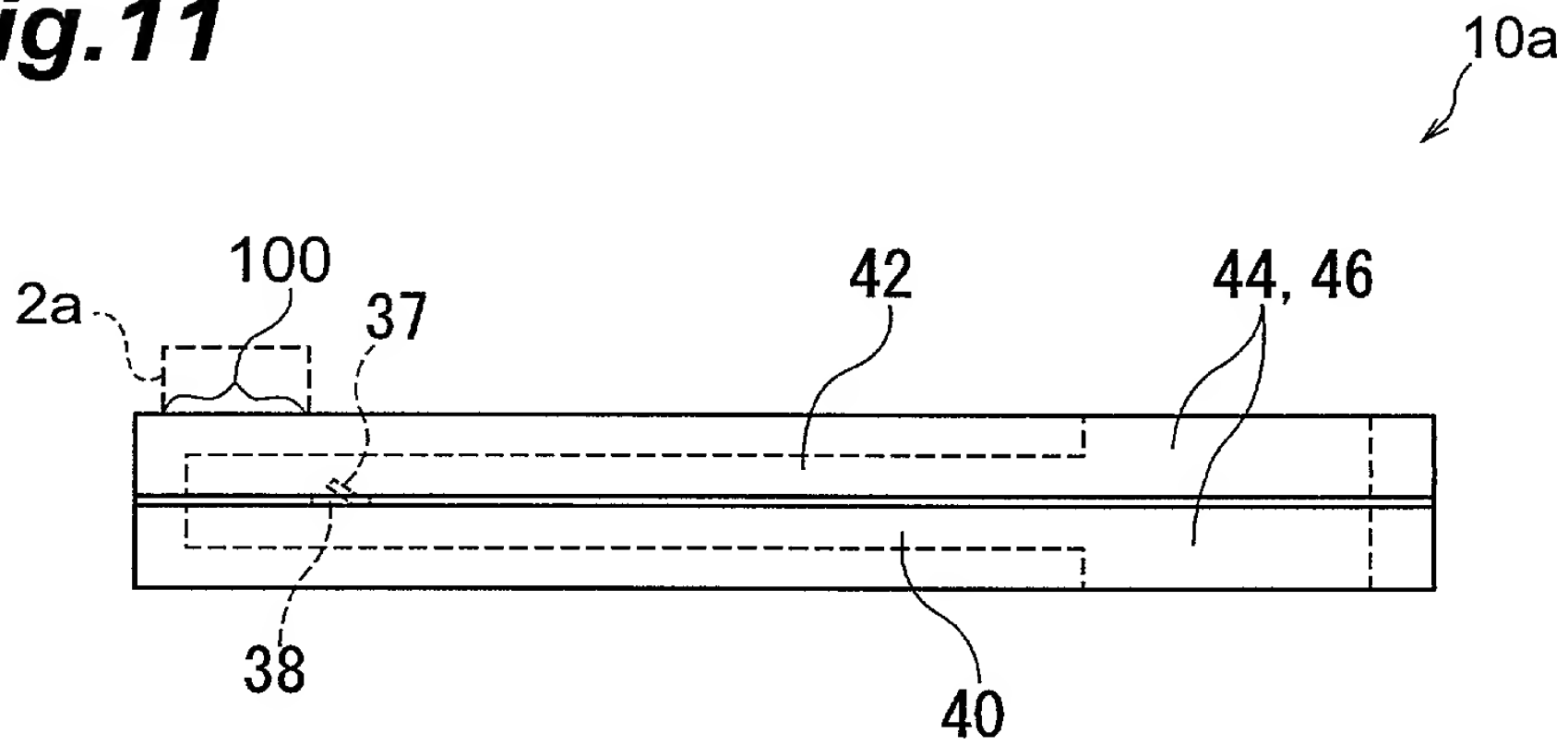
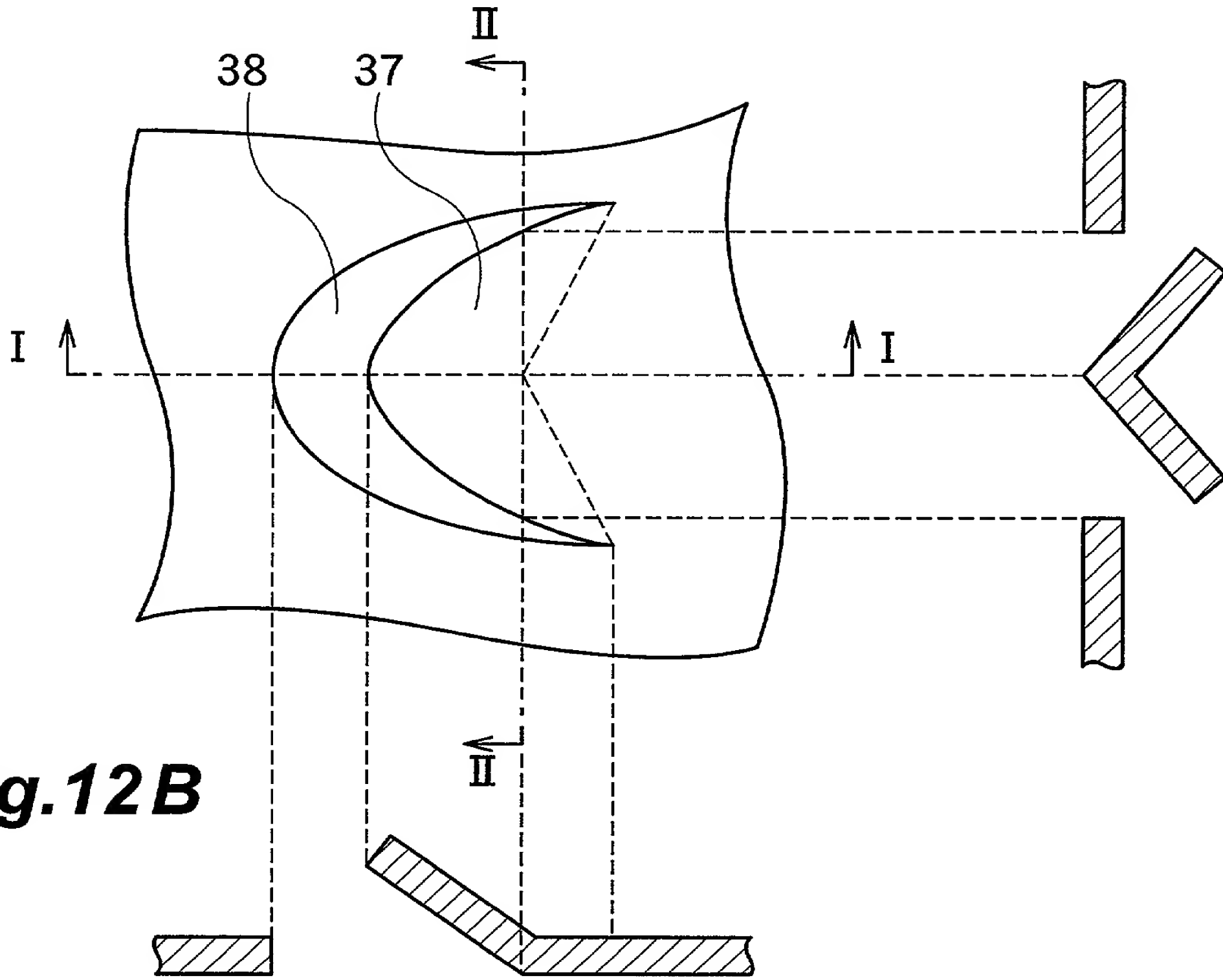


Fig.12A

Fig.12C



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Fig. 13A

FIG. 13B is a perspective view of the device 12 from a different perspective.

Fig. 13B

